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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Lizy Kurian John 02/17/98
SERIAL NUMBER: 08/512,505
FILED: August 8, 1995 RECEIVED
EXAMINER: T. Fears FEB 20 1998
GROUP ART UNIT: 2511 GROUP 2500
FOR: COMPUTER MEMORY CHIP WITH FIELD
PROGRAMMABLE MEMORY CELL ARRAYS (FPMCas)
AND METHOD OF CONFIGURING
ATTORNEY DOCKET NO.: 2115.007

The Commissioner of Patents
and Trademarks
Washington, D.C. 20231

AMENDMENT A

Sir:

In response to the first Office Action, please amend
the above-identified application as follows:

In the Claims

Please cancel Claims 1-11 without prejudice or disclaimer
and insert new claims as follows:

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wth*
-12. A field programmable memory cell array including a
memory array composed of a sea of N memory cells addressed by a
row decoder and a column decoder to input or output data by means
of input/output circuits, input data control circuits and output
data control circuits as determined by select/write enable logic,
comprising in combination a programmable address decoder and a
programmable interconnect for selective reconfiguration of the